

APR 03 2007

Application No.: 10/701,261

Docket No.: JCLA7806-R

REMARKSClaim Objections

Claim 5 is objected to because of a typo error.

In response thereto, Applicants have amended claim 5 as instructed by the Examiner. As such, claim 5 is now submitted to be in allowable form.

Claim Rejections 35 U.S.C. 102

Claims 1 and 3-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Jennings (US 6,697,416).

In response to the rejections thereto, Applicants hereby otherwise traverse these rejections. As such, Applicants submit that claims 1 and 3-7 are novel and unobvious over Jennings, or any of the other cited references, taken alone or in combination, and thus should be allowed.

With respect to claim 1, as previously presented, recites the limitation of "wherein the control chip is able to spread out the frequency of an **electromagnetic interference signal** according to an algorithm" (Emphasis added).

Applicants submit that Jennings fails to teach the claimed limitation of "wherein the control chip is able to spread out the frequency of an electromagnetic interference signal according to an algorithm". Throughout the Jennings reference, it is taught that Jennings was teaching to spread out the input clock signal  $F_{CLK}$ , rather than "frequency of an electromagnetic interference signal" as required by the claimed invention. Although Jennings also concerns to reduce EMI, he

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endeavors in dealing with the input clock signal by “providing a digital programmable SSCG that produces a precise output waveform with a low spectral density for reduced EMI with very low noise or jitter” (col. 4, lines 50-54). Therefore, it is concluded Jennings concern to operate on the entire input signal, rather than the **“the frequency of an electromagnetic interference signal”**.

For failing to teach each and every limitation, Jennings does not anticipate the represent invention, as set forth in claim 1.

Similarly, claim 3, recites the limitation of “a software phase lock loop ... receiving a clock signal and **spreading out the frequency of an electromagnetic interference signal** according to an algorithm received from an external bus, wherein **the electromagnetic interference signal at each frequency are modulated** according to a corresponding spread out width” (Emphasis added);

Claim 5, recites the limitation of “a software phase lock loop ... for **spreading out the frequency of an electromagnetic interference signal** according to the clock signal and the algorithm, wherein **the electromagnetic interference signal at each frequency are modulated ...**” (Emphasis added); and

Claim 7, recites the limitation of “determining a specified **frequency of the electromagnetic interference signal ... and spreading out the electromagnetic interference signal**” (Emphasis added).

Applicants submit that each of claims 3, 5 and 7 contains limitation of **“spreading out the frequency of an electromagnetic interference signal”** that is novel and unobvious over Jennings.

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As such, claims 3, 5, and 7 are novel and unobvious over Jennings or any of the other cited references, taken alone or in combination, and thus should be allowed.

Claim 4 depend on allowable claim 3, and claim 6 depends on allowable claim 5, and thus claims 4 and 6 should also be allowable.

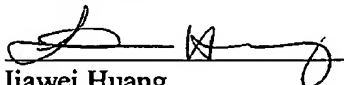
### CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims 1, and 3-7 are in proper condition for allowance and an action to such effect is earnestly solicited. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date: 4/3/2007

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Respectfully submitted,  
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